



S&H Form: (2/01)  
DOCKET NO. 300.1180

*JFW*

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Haruo SORIMACHI

Serial No: 10/521,195

Group Art Unit: 2829

Confirmation No. 2210

Filed: January 13, 2005

Examiner: To be Assigned

For: SEMICONDUCTOR DEVICE AND INTERPOSER

**REQUEST FOR CORRECTED FILING RECEIPT**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450  
55555552  
Sir:

It is requested that the name and address of Assignee be added to the Official Filing Receipt. The name and address of the Assignee is:

**SHINKO ELECTRIC INDUSTRIES CO., LTD.**

**Nagano, Japan**

as is evidenced by the Assignment, attached to the application as filed. It is also requested that the number of drawings on the Official Filing receipt be corrected. The correct number of drawing is **6**, as is evidenced by a stamped copy of the postcard receipt, attached to the application as filed. For the convenience of the Patent and Trademark Office, attached is a photocopy of the original receipt on which the errors have been noted in red.

It is requested that a corrected Official Filing Receipt be issued in this application.

Respectfully submitted,

STAAS & HALSEY LLP

Date: Jan. 20, 2006

By: *H. J. Staas*  
H. J. Staas  
Registration No. 22,010

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OIPE 14P45  
JAN 20 2006  
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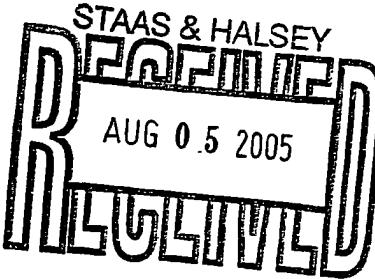
## UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/521,195	01/13/2005	2829	900	300.1180	5	10	2

CONFIRMATION NO. 8654

21171  
STAAS & HALSEY LLP  
SUITE 700  
1201 NEW YORK AVENUE, N.W.  
WASHINGTON, DC 20005



## FILING RECEIPT



\*OC000000016585648\*

Date Mailed: 07/22/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Haruo Sorimachi, Nagano-shi, JAPAN;

-- Assignment For Published Patent Application --  
--SHINKO ELECTRIC INDUSTRIES CO., LTD.  
NAQANO JAPAN--

Power of Attorney: The patent practitioners associated with Customer Number 21171.

## Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/05353 04/15/2004

## Foreign Applications

JAPAN 2003-136863 05/15/2003

Projected Publication Date: 10/27/2005

Non-Publication Request: No

Early Publication Request: No

## Title

Semiconductor device and interposer

**Preliminary Class**

324

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Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

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## U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter "ASSIGNOR") by

(Insert  
ASSIGNEE's  
Name(s)  
Address(es))

SHINKO ELECTRIC INDUSTRIES CO., LTD.

80, Oshimada-machi, Nagano-shi, Nagano 381-2287, Japan

(Title of  
Invention)

## SEMICONDUCTOR DEVICE AND INTERPOSER

relating to International Patent Application PCT/JP 2004/005353 and/or for which application for Letters Patent of the United States was executed on even date herewith or, if not so executed, was:

(Insert date  
of execution  
of application,  
if not  
concurrent)

(a) executed on \_\_\_\_\_;

(\_\_\_\_\_) is hereby authorized to insert in (b) the specified data, when known.

and to said application and all Letters Patent(s) of the United States granted on said application and any continuation, division, renewal, substitute, reissue or reexamination application based thereon, for the full term or terms for which the said Letters Patent(s) may be granted and including any extensions thereof (collectively, hereinafter, "said application(s) and Letters Patent(s)").

The ASSIGNOR agree(s), when requested by said ASSIGNEE and without charge to but at the expense of said ASSIGNEE, to do all acts which the ASSIGNEE may deem necessary, desirable or expedient, for securing, maintaining and enforcing protection for said invention, including in the preparation and prosecution of said application(s) and the issuance of said Letters Patent(s), in any interference, reissue, reexamination, or public use proceeding, and in any litigation or other legal proceeding which may arise or be declared in relation to same, such acts to include but not be limited to executing all papers, including separate assignments and declarations, taking all rightful oaths, providing sworn testimony, and obtaining and producing evidence.

IN WITNESS WHEREOF, the undersigned inventor(s) has (have) affixed his/her/their signature(s).

**(Signatures)**

1. Haruo Sorimachi Haruo Sorimachi December 3, 2004  
(SIGNATURE) (TYPE NAME) (DATE)

2] \_\_\_\_\_ (SIGNATURE) \_\_\_\_\_ (TYPE NAME) \_\_\_\_\_ (DATE)

3] \_\_\_\_\_ (SIGNATURE) \_\_\_\_\_ (TYPE NAME) \_\_\_\_\_ (DATE)

4) \_\_\_\_\_ (SIGNATURE) \_\_\_\_\_ (TYPE NAME) \_\_\_\_\_ (DATE)

5) \_\_\_\_\_ (SIGNATURE) \_\_\_\_\_ (TYPE NAME) \_\_\_\_\_ (DATE)

Please Date Stamp and return

NEW NATIONAL PHASE PATENT APPLICATION INCLUDING: TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371; Translated Application, claims, and abstract (22) pages; (6) sheets of Drawings (FIGS. 1-2, 3A, 4-11); Declaration; Assignment along with Recordation Cover Sheet; Preliminary Amendment; Information Disclosure Statement, PTO-1449 and (3) references; International Search Report (Form PCT/ISA/210); Non-English document P732-PCT; and Cover Sheet of PCT-WO 2004/102653, and CHECK for \$1,040.00

APPLICANT(S):

Yoshihito YAGINUMA et al

SERIAL NO:

To be Assigned

CONFIRMATION NO. To be Assigned

TITLE: SEMICONDUCTOR DEVICE AND INTERPOSER

FILING DATE: January 13, 2005

DOCKET NO: 300.1180/HJS:rpa

DUE DATE: January 13, 2005



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